

Figure 1A

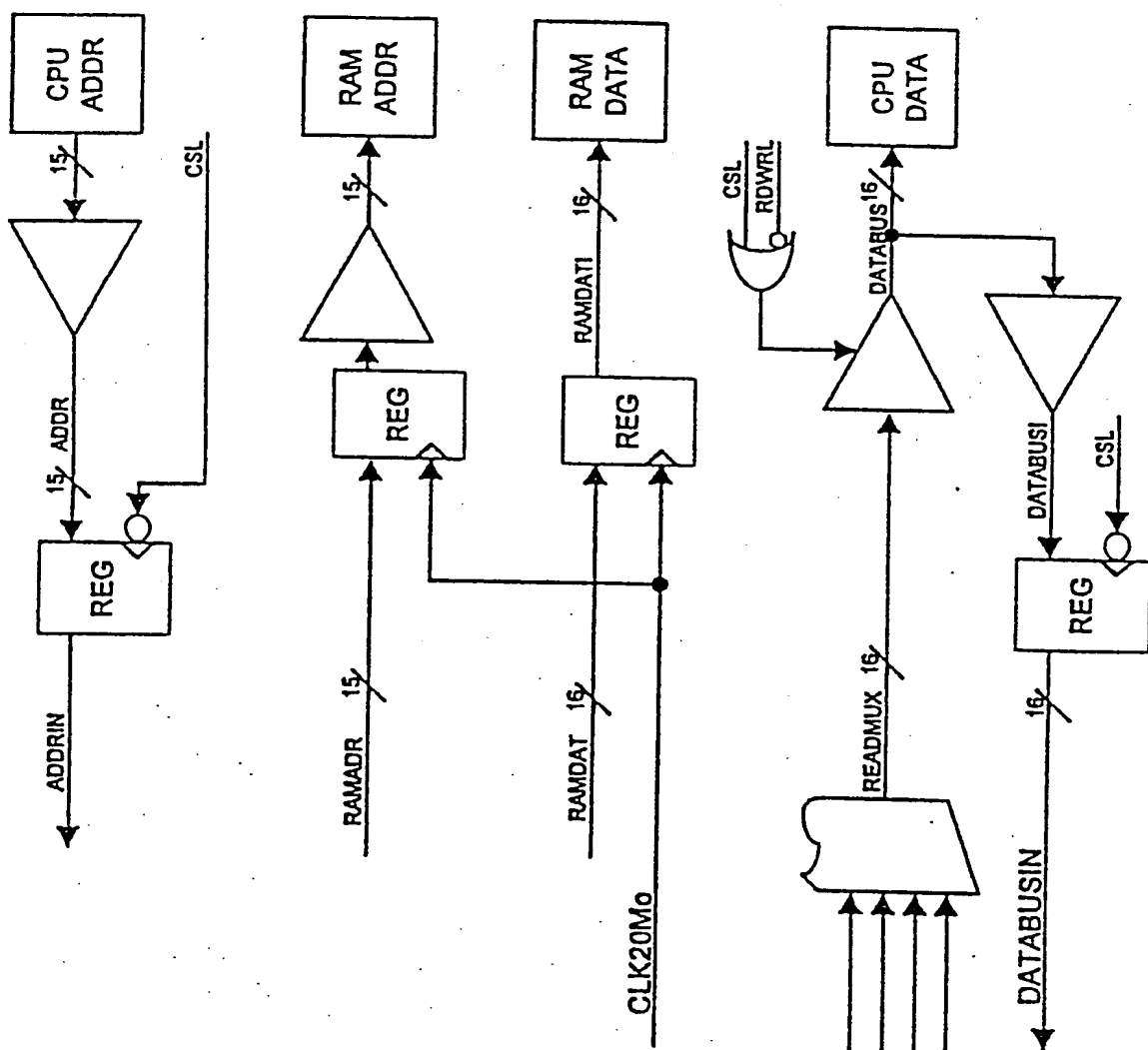


Figure 13

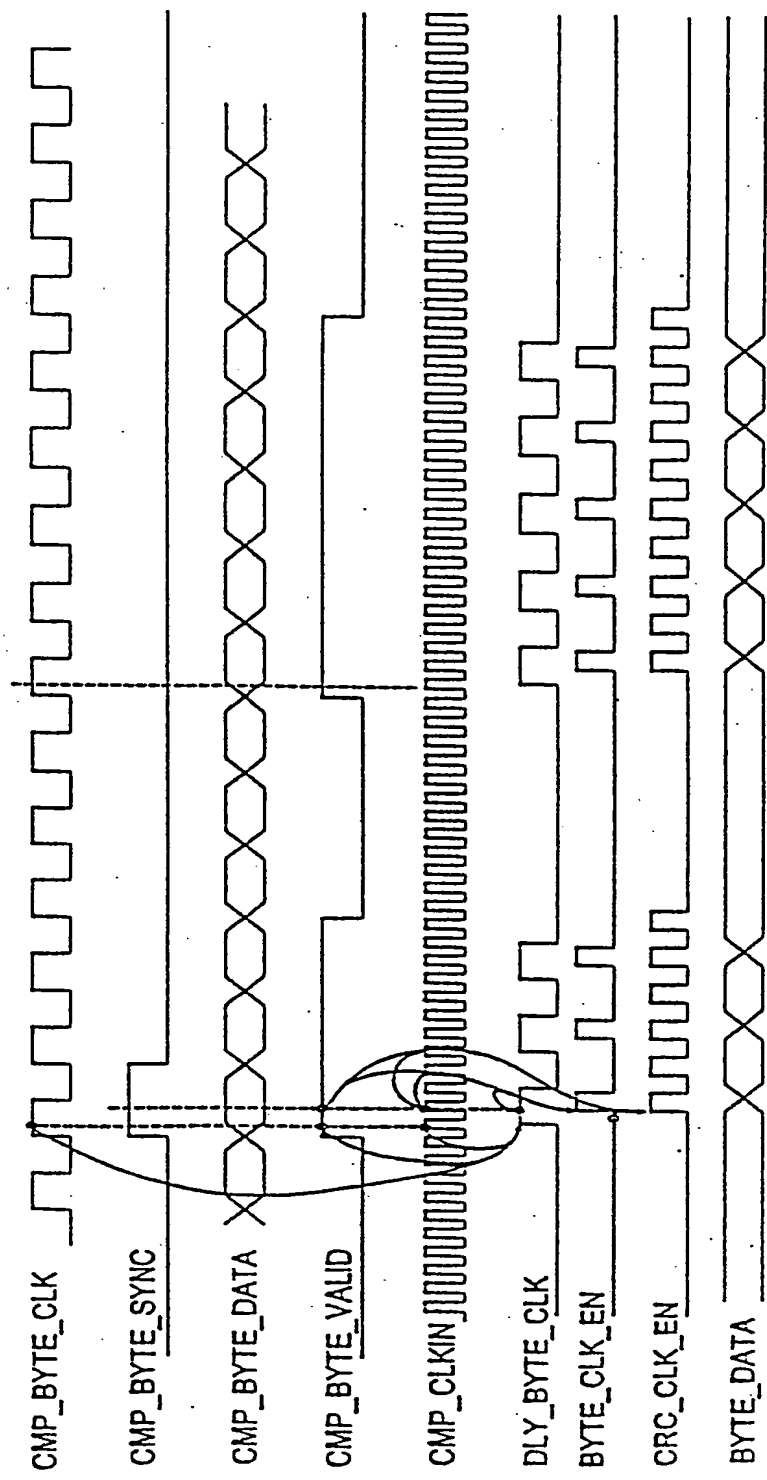


Figure 2

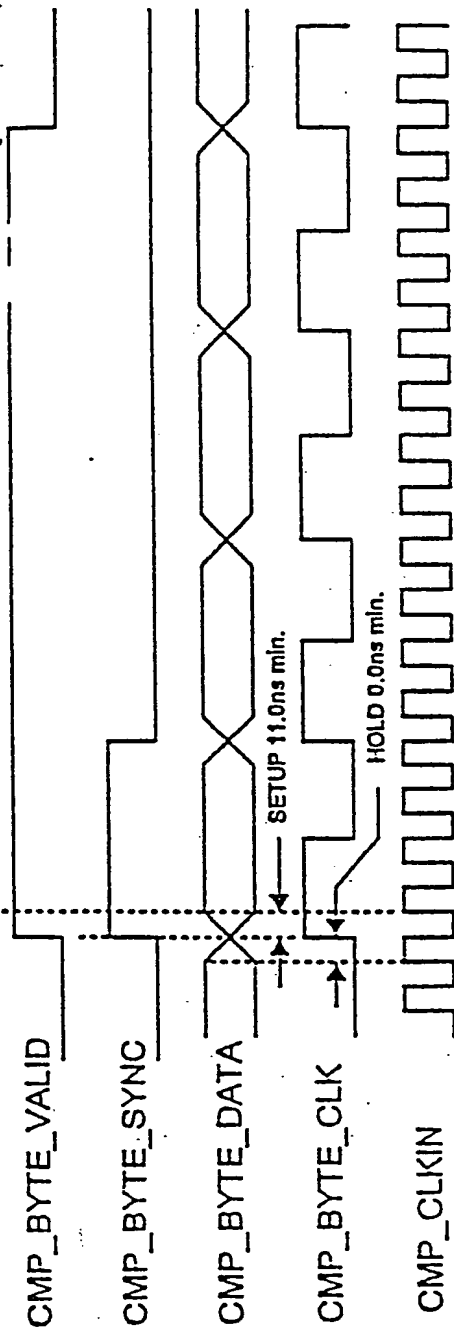


Figure 3

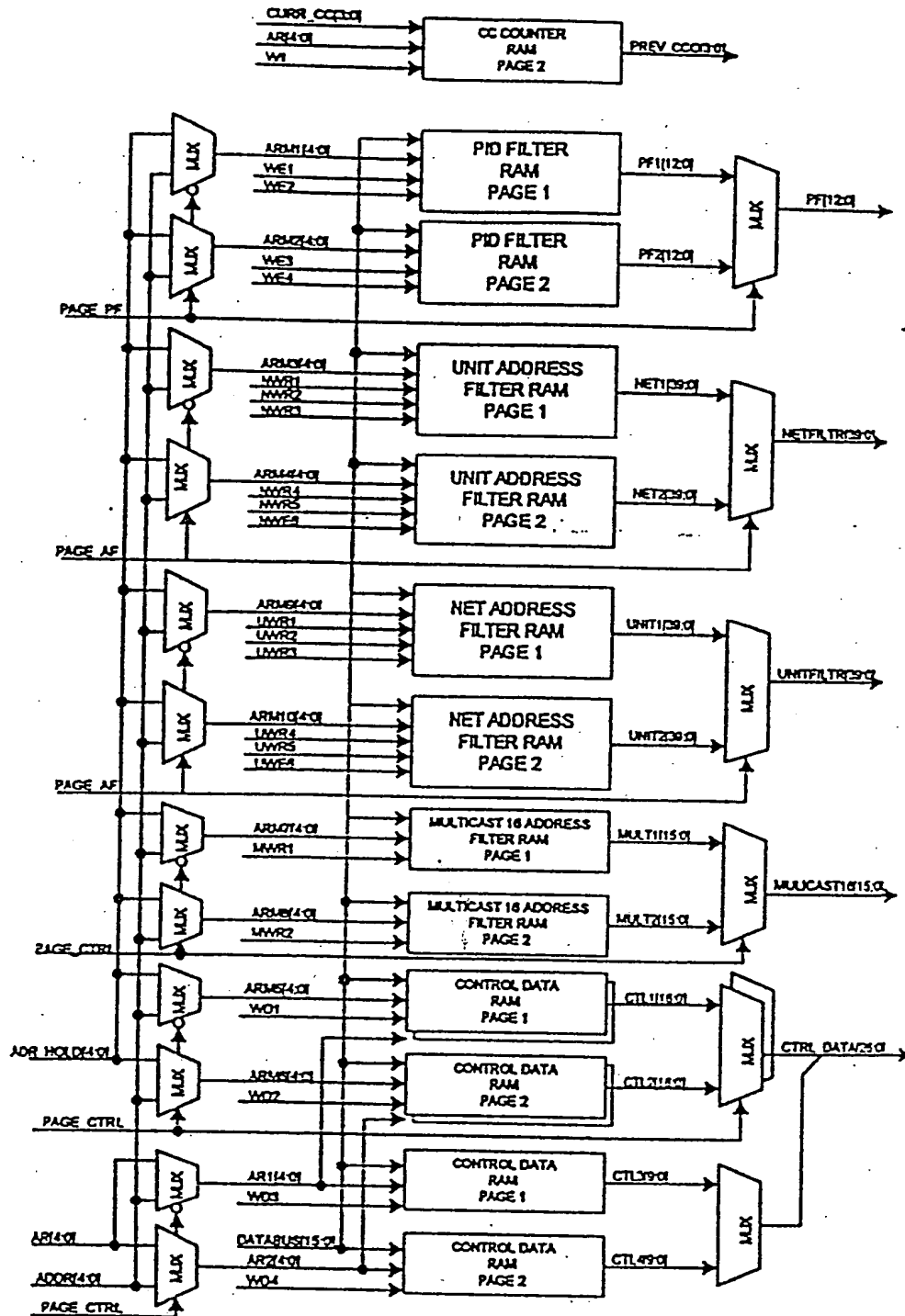


Figure 4A

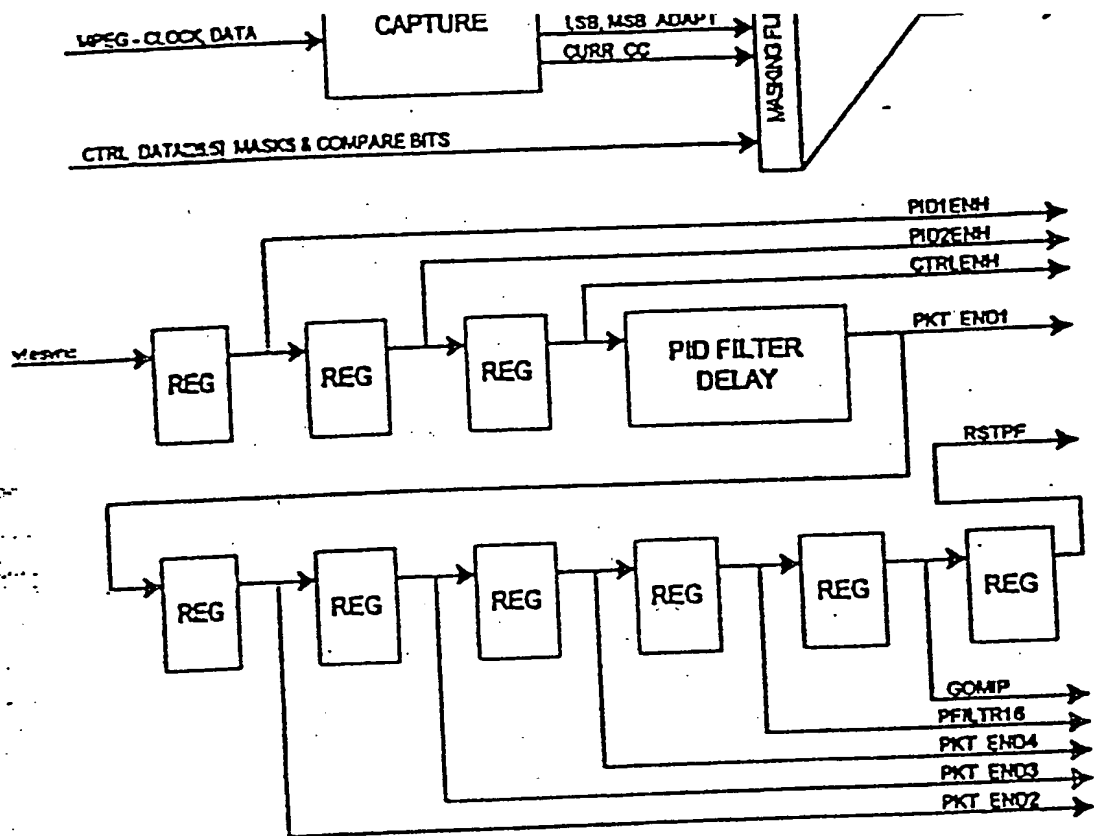
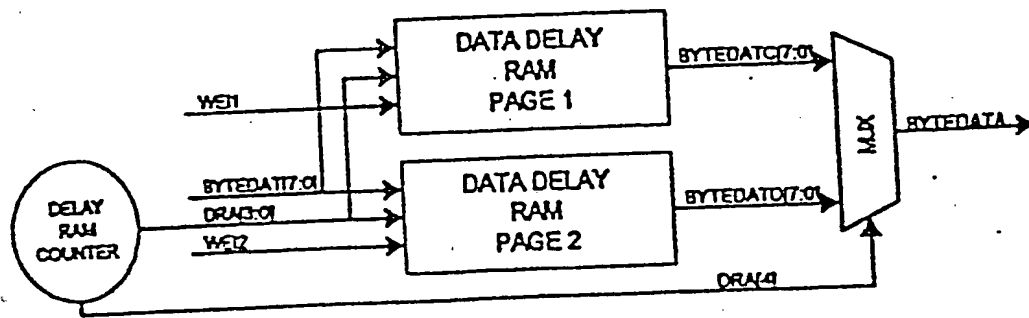
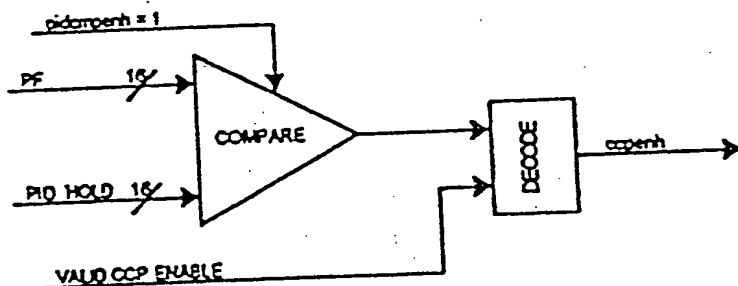
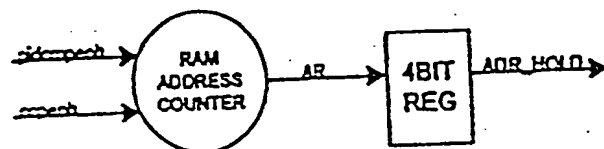


Figure 4B



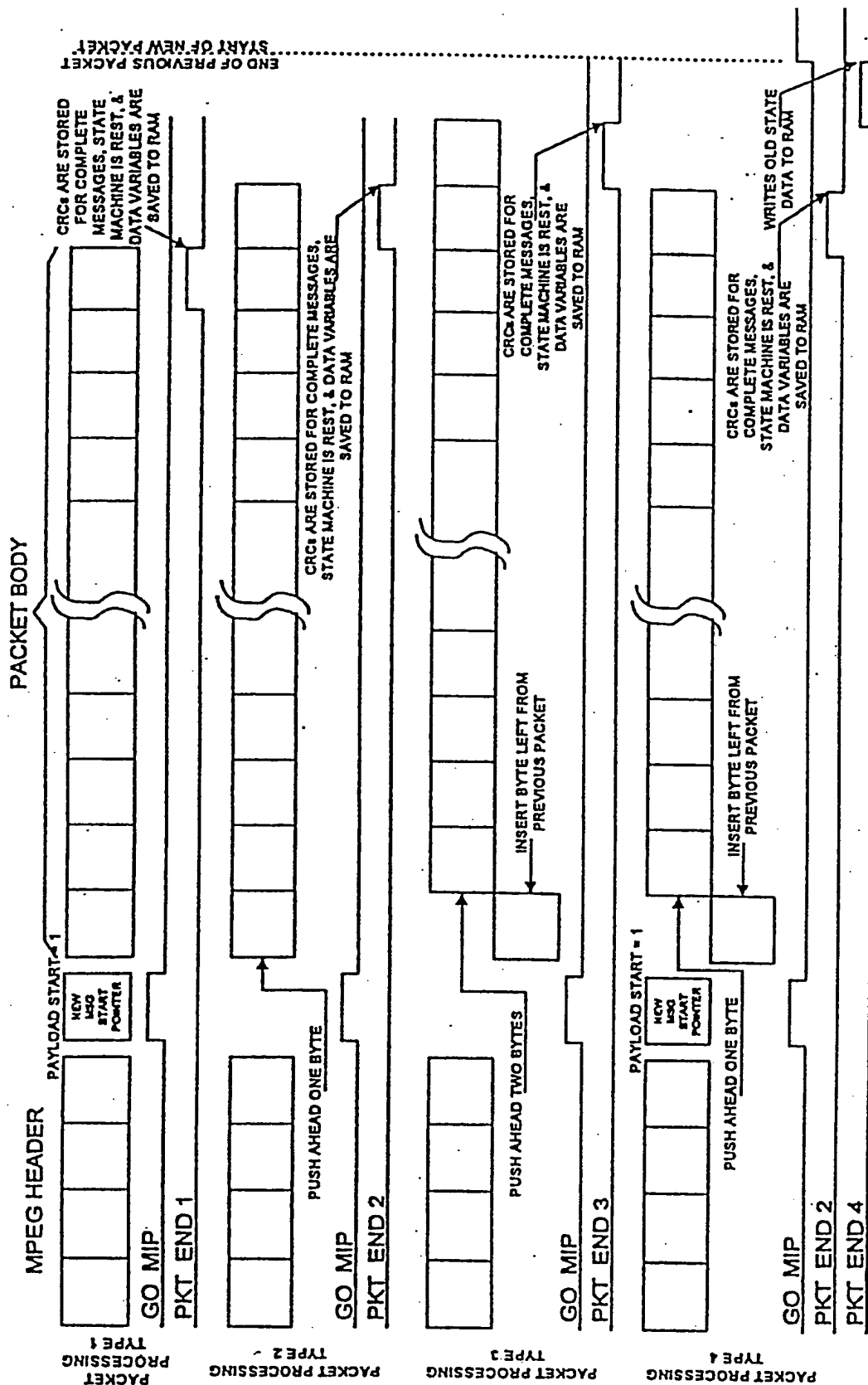


Figure 5

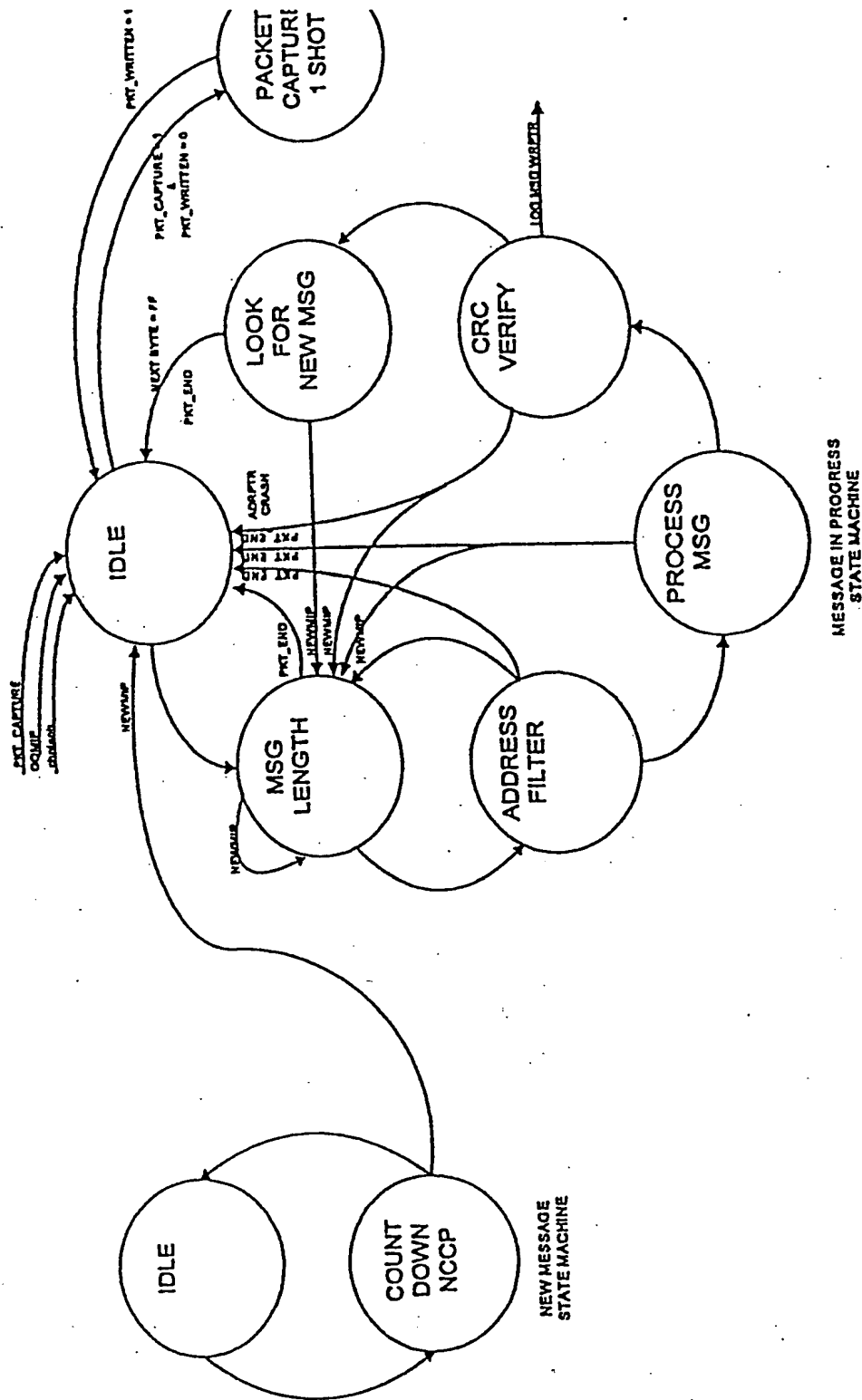


Figure 6

All RAMs are synchronous to the system clock

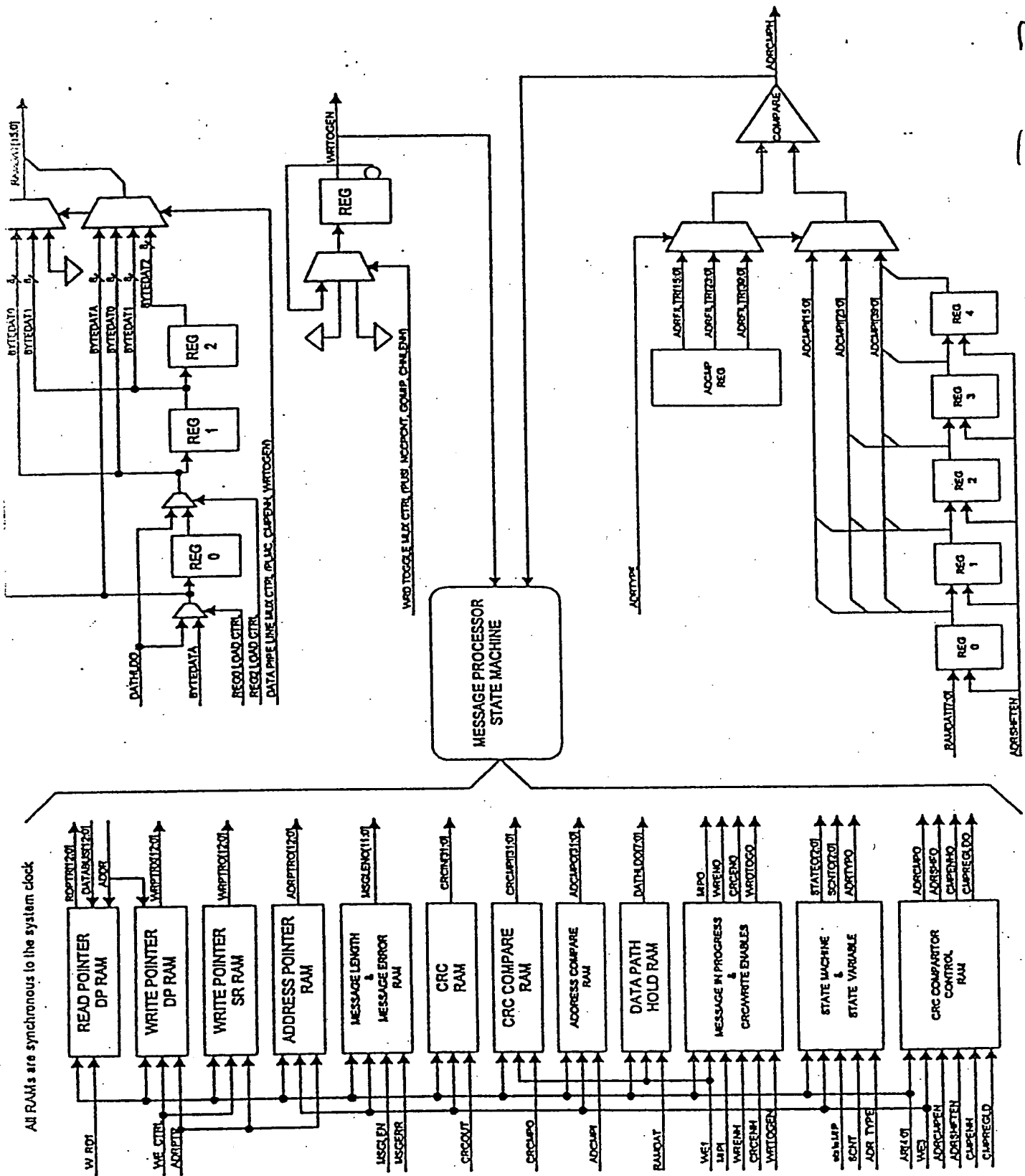


Figure 7

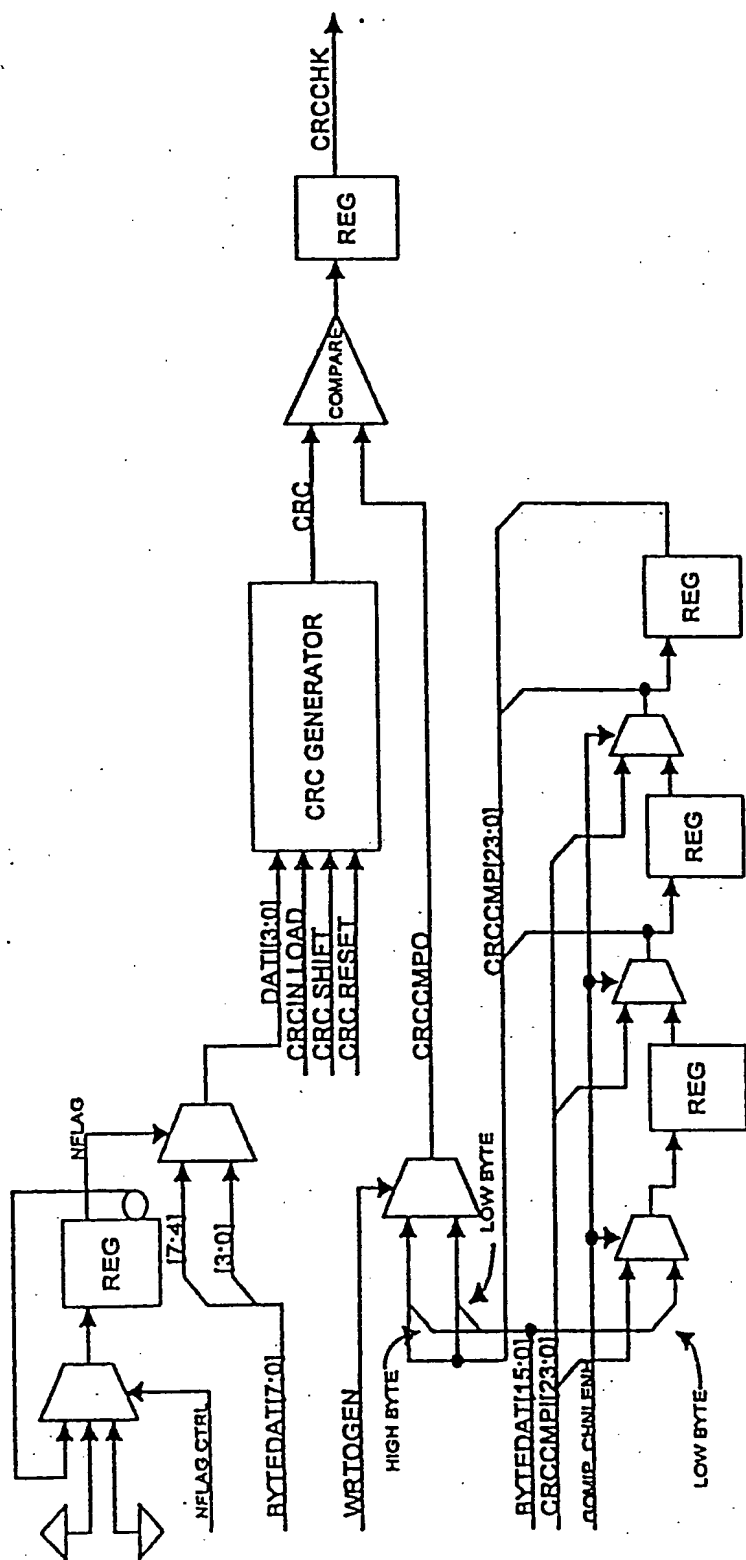


Figure 9

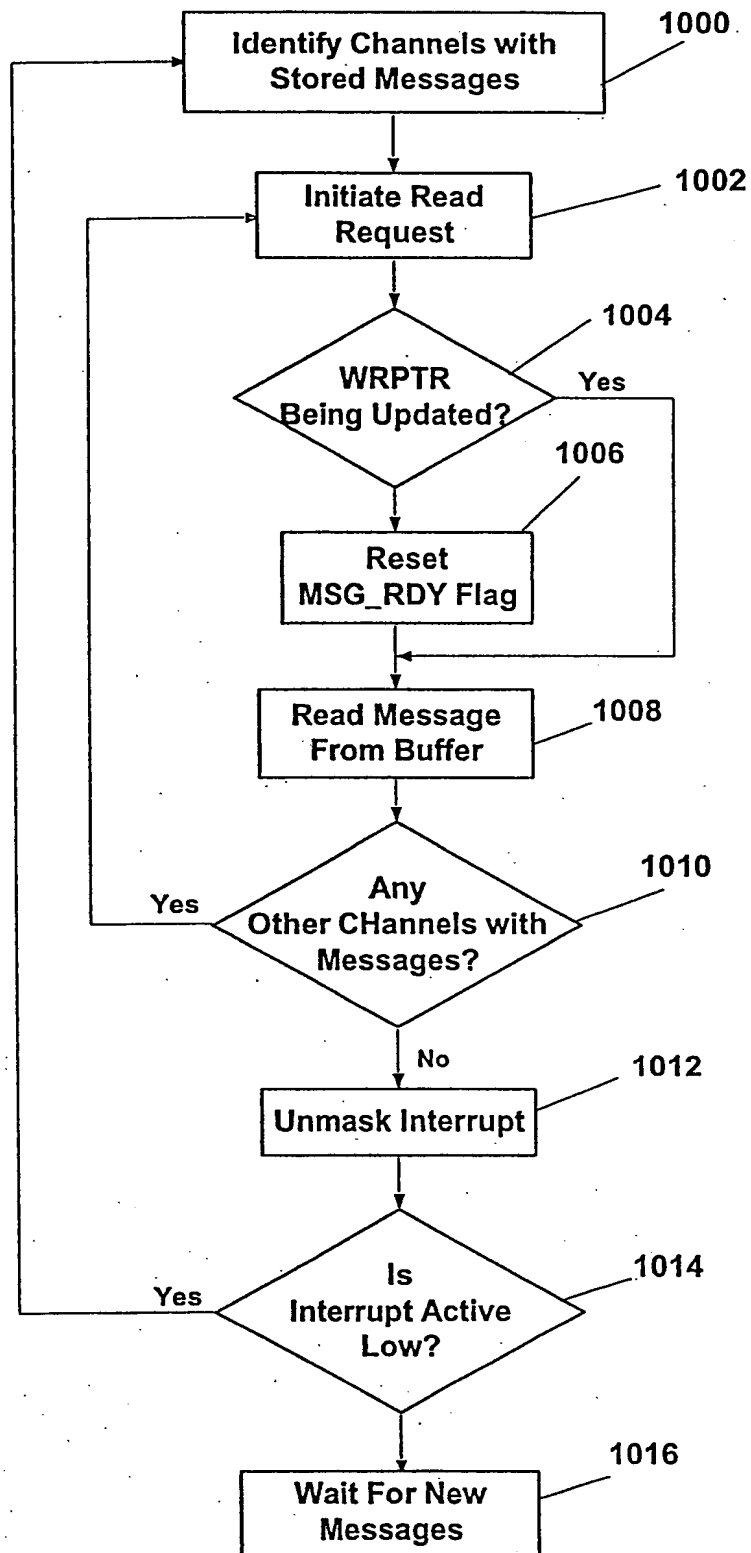


Fig. 10

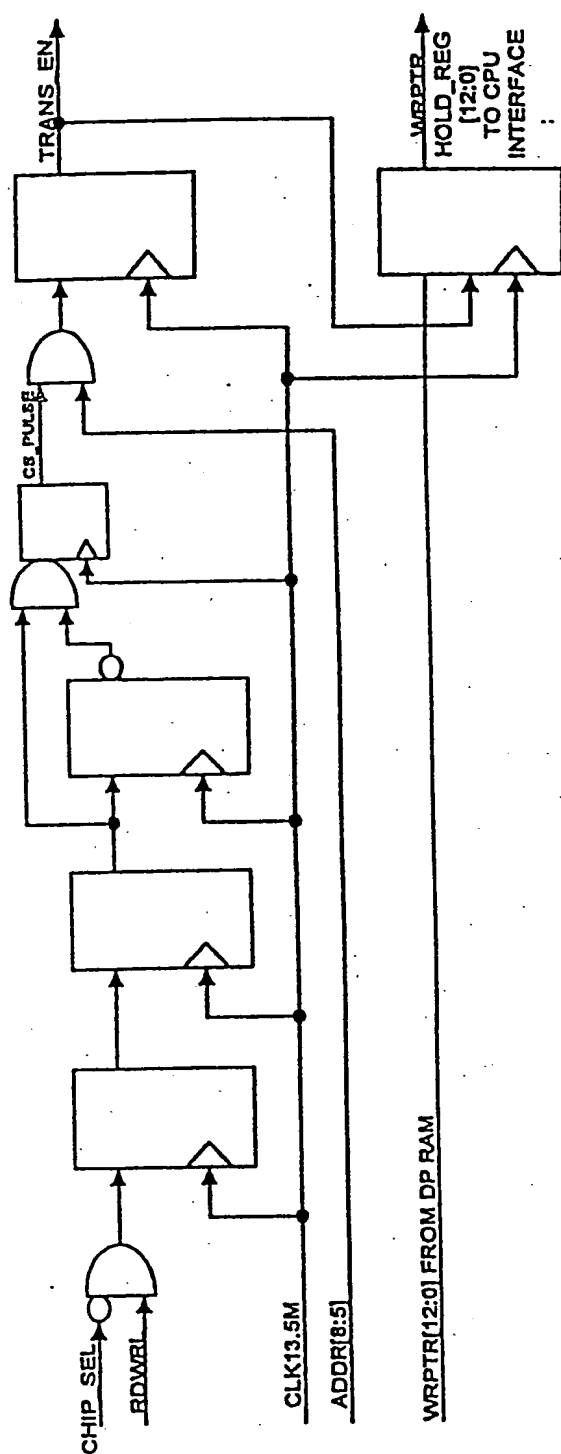


Figure 11

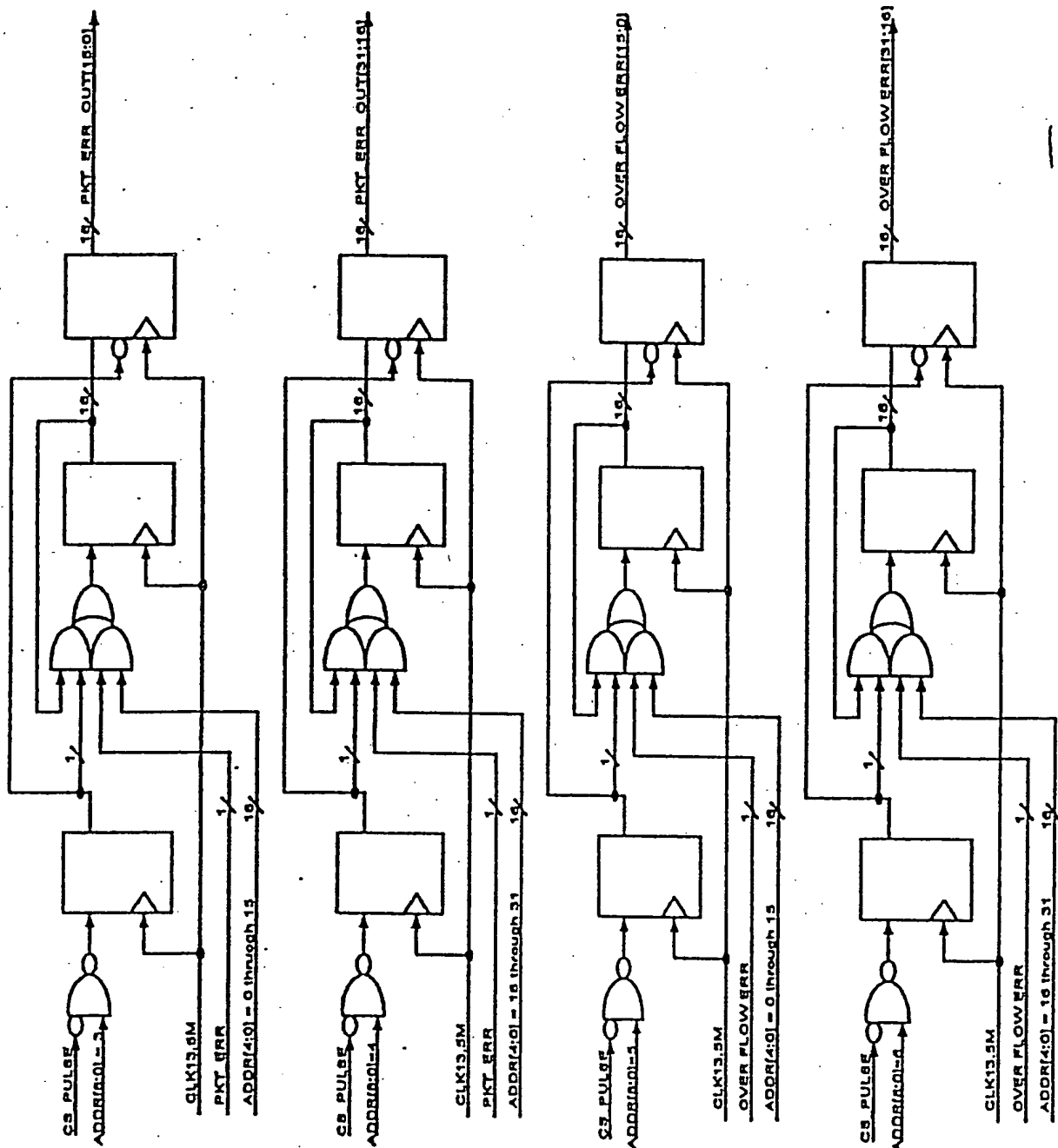


Figure 12